

S/N 10/028001

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Tu-Tu V. Ho

Serial No.: 10/028001

Group Art Unit: 2818

Filed: December 20, 2001

Docket No.: 1303.035US1

Title: PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL  
DEVICES AND ASYMMETRICAL TUNNEL BARRIERS

PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Please amend the above-identified application as follows:

**Match and Return**